IN THE SPECIFICATION

Please replace the paragraph appearing at page 11, lines 7-13 with the following:

The number of bits of address that is provided by the look-up ROM 607 of Fig. 6 depends on the number of samples that are used to define the path between symbols. If 6 symbols are used, as in the present embodiment, then the sample counter 602 would need to provide 18 bits of address to the look-up ROM. As the symbols are accepted, the successive values for I and Q are accessed from the ROM 607. All the successive I and Q values to traverse from any one symbol to any allowed next symbol are contained within the look-up ROM 613.